

10/058557

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,608,370 B1
DATED : August 19, 2003
INVENTOR(S) : Chen et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14,

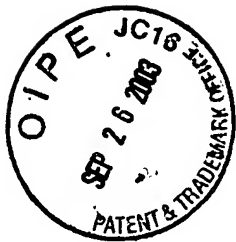
Line 26, after "semiconductor", insert -- material, --.

Signed and Sealed this

Fourth Day of November, 2003

A handwritten signature in black ink, appearing to read "James E. Rogan", with a long horizontal flourish underneath.

JAMES E. ROGAN
Director of the United States Patent and Trademark Office



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Certificate

OCT 02 2003

of Correction

APPLICANTS: Chen et al. SERIAL NO.: 10/058,557
PATENT NO.: 6,608,370 B1 FILED: January 28, 2002
ISSUED: August 19, 2003 CASE NO.: AP01983
ENTITLED: SEMICONDUCTOR WAFER HAVING A THIN DIE AND TETHERS AND
METHODS OF MAKING THE SAME

Motorola, Inc.
Corporate Offices
1303 E. Algonquin Road
Schaumburg, IL 60196
September 23, 2003

REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 CFR 1.322 of the Rules of Practice, which implement 35 USC 254, the Patent and Trademark Office is respectfully requested to issue a Certificate of Correction in the above-identified patent.

It is certified that errors appear in the above-identified patent as shown in the attached Certificate of Correction.

Applicant certifies that the errors are of a minor character and were not the fault of Applicant. Since the changes necessary to correct these errors in the patent would not constitute new matter, and would not require re-examination, Applicant prays a Certificate of Correction will issue.

Since errors were not the fault of Applicant, it is believed that there will not be a fee for this Certificate of Correction.

Please forward all correspondence to:
CUSTOMER NUMBER 22917

Respectfully submitted,
Shiuh-Huo Steven Chen et al.

I hereby certify that this correspondence is being deposited with the United States

Postal Service as first class mail in an envelope addressed to:

Commissioner of Patents and Trademarks, P.O. Box 1450

Alexandria, VA 22313-1450

On September 23, 2003

(Date of Deposit)

Motorola, Inc

Name of applicant, assignee, or Registered Rep.

Signature 9/23/03 Date

By

Brian M. Mancini
Brian M. Mancini
Attorney for Applicants
Registration No. 39,288
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Phone: (847) 576-3992
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OCT 02 2003

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.
(Also Form PTO-1050)

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MAILING ADDRESS OF SENDER;
Brian M. Mancini
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PATENT NO. 6,453,155 B1

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